



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4031

SERIAL NUMBER 10/668,166	FILING DATE 09/24/2003  RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 1035-473
-----------------------------	---------------------------------------	--------------	------------------------	---------------------------------

## APPLICANTS

Toshio Kimura, Kitakatsuragi-gun, JAPAN;

Yoshihisa Dotta, Nara-shi, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

n/a A.O.A.

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-313530 10/28/2002 A.O.A.

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 12/19/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

## ADDRESS

23117  
 NIXON & VANDERHYE, PC  
 901 NORTH GLEBE ROAD, 11TH FLOOR  
 ARLINGTON, VA  
 22203

## TITLE

Semiconductor device and chip-stack semiconductor device

FILING FEE  RECEIVED 918	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	---